

Article

# Real-Time Reliability Monitoring of DC-Link Capacitors in Back-to-Back Converters

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**Abstract:** Electrolytic capacitors have large capacity, low price, and fast charge/discharge characteristics. Therefore, they are widely used in various power conversion devices. These electrolytic capacitors are mainly used for temporary storage and voltage stabilization of DC energy and have recently been used in the renewable energy field for linking AC/DC voltage and buffering charge/discharge energy. However, electrolytic capacitors continue to be disadvantageous in their reliability due to their structural weaknesses due to the use of electrolytes and very thin oxide and dielectric materials. Most capacitors are considered a failure when the capacitance has changed by 25% of its initial value. Accurate and fast monitoring or estimation techniques are essential to be used with low cost and no extra hardware. In order to achieve these objectives, an online, reliable, and high-quality technique that continuously monitors the DC-link capacitor condition in a three-phase back-to-back converter is proposed. In this paper, the particle swarm optimization (PSO)-based support vector regression (PSO-SVR) approach is employed for online capacitance estimation based on sensing or deriving the capacitor current. Because the SVR performance alone severely depends on the tuning of its parameters, the PSO algorithm is used, which enables a fast online-based approach with high-parameter estimation accuracy. Experimental results are provided to verify the validity of the method.

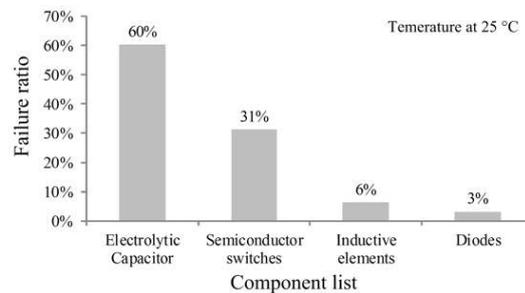
**Keywords:** electrolytic capacitors; PSO; PSO-SVR; ESR

## 1. Introduction

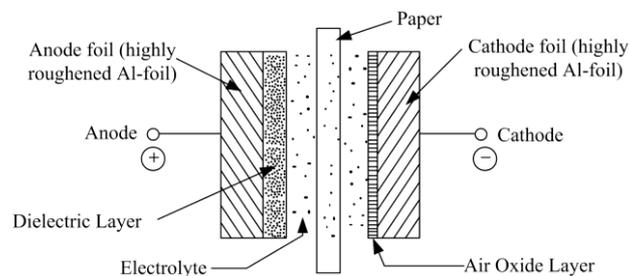
Nowadays, one of the most important parts of power electronic systems is the electrolytic capacitors, which are used to smooth voltage because it has high capacitance for its size and low price. The high energy density ( $\text{J}/\text{cm}^3$ ) characteristics give it special advantages for smoothing voltage oscillation and pulse discharge circuitry. On the other hand, a disadvantage is its short lifetime, which is considered the shortest in power electronic circuits. At an ambient temperature of 25 °C and under the rated conditions, electrolytic capacitors' failure is highest among all capacitor types [1,2]. Figure 1 shows the failure ratio of different parts in power electronics systems [3]. The wear-out process of electrolytic capacitors is due to the evaporation and deterioration of the electrolyte through the seals. Therefore, an oscillation of the capacitor's internal equivalent series resistance (ESR) can occur [4–7].

The aluminium electrolytic capacitor consists of a cathode aluminium foil, an anode foil, and a separator plate wound together and soaked with electrolyte as shown in Figure 2. The main components of the liquid electrolyte are solvent, solute, such such as ethylene glycol and ammonium borate, water, corrosion inhibitors, and hydrogen absorbers. In case of heating, the capacitor internal temperature

increases and rushes evaporation of the capacitor's electrolyte. Therefore, the volume of the electrolyte is reduced so that the etched tunnels are not fully filled due to the electrolyte evaporation. Hence, the capacitance decreases as a result of the electrolyte volume reduction. The evaporation process will be higher whenever the ambient temperature increases; then, the capacitance value decreases and the lifetime deteriorated more.



**Figure 1.** Distribution of failure of power components.



**Figure 2.** Structure of electrolytic capacitor.

The normal lifetime for the electrolytic capacitors operating at maximum allowed core temperature are typically 1000 to 10,000 hours, which is almost 1 year maximum. This lifetime is short for most of power electronics systems and makes it the weakest part [8]. Therefore, a wearing-out detection method of the electrolytic capacitor is essential for protective maintenance of the power electronics circuit.

The aluminum electrolytic capacitor consists of two aluminum sheets and an electrolytic solution as shown in Figure 2. During the process of forming a capacitor, an electrochemical reaction is generated on the surface of the anode through a molding process, and as a result, an oxide layer is formed to prevent the two metal plates from contacting each other. The effective cross-sectional area can be increased by etching the anode surface during the manufacturing process. The two separate aluminum plates are rolled into a cylindrical shape to minimize volume. Through this manufacturing process, the thickness between the poles can be thinned and the cross-sectional area can be made thick, and as a result, the capacity per unit volume of the capacitor can be increased. Also shown also in Figure 2 is an oxide layer is formed on the cathode surface through a natural chemical reaction between the electrolyte and aluminum. Compared to the thickness of the anode surface oxidation layer formed by the manufacturing process, the capacitance on the cathode side is larger than that on the anode side. Therefore, since the anode and cathode capacitors can be regarded as connected in series, the total capacitance becomes similar to the capacitance value of the anode. Equivalent series resistance ( $R_s$ ) represents the sum of all the resistances of the electrolyte, oxide layer, electrode, lead, and connection. The equivalent inductance ( $L_s$ ) is the inductance formed by winding the coil in a cylindrical shape, which can usually be ignored in the frequency range of the power converter. Thus, the equivalent circuit can be simplified in the form of a series connection of the resistor and the capacitor.

Due to the structural characteristics of the capacitor, the capacitor can be electrically modeled as a series connection of capacitors, resistors, and inductors [9]. The major eroding mechanisms in these capacitors are the leakage and deterioration of the electrolyte by vapor diffusion through the seals. Such

deterioration causes a fluctuation of the capacitor's internal ESR [10–13]. Several methods to estimate the deterioration status of the electrolytic capacitor have been proposed in the literature [14,15].

In [14], ESR was estimated by various methods such as fundamental wave, filter use, and complicated signal processing from voltage and current across the capacitor. However, since there is no sensor for measuring the charge/discharge current of the capacitor in the power conversion device, there is a disadvantage that additional hardware must be added in order to implement the above method. In [15], the capacitance is estimated from the injected ripples on the DC-link, and the lifetime of the DC capacitor is determined without additional hardware.

In [9], ESR of the capacitor was estimated using only the DC voltage and the phase current sensor. However, in order to measure the rapid charge/discharge change of the voltage during the inverter switching period, A/D with a higher sampling frequency than the device is required. Reference [16] proposes ESR calculation technique by measuring both capacitor voltage and current. The disadvantage of this method is the effect of current sensor on the waveforms, which is unavoidable.

Recently, artificial intelligent and machine learning methods have been implemented widely for monitoring the DC-link capacitor's condition. Capacitance estimation in back-to-back converters using machine learning techniques such as the artificial neural network (ANN) method has been investigated in [17]. To implement ANN-based methods and to ensure accurate capacitance estimation, the number of neurons and hidden layers should be large, which causes the process to take more time in the training stage. The support vector machine (SVM) is a powerful machine learning technique that implements structural risk minimization instead of the empirical risk minimization. It overcomes ANN prediction and classification problems, non-linear function problems, and loss functions [18]. Support vector regression (SVR) is also an efficient machine learning technique due to its accuracy, which is influenced by the parameter selection.

In this study, an improved real-time capacitance monitoring based on PSO and SVR, namely particle swarm optimization-based support vector regression (PSO-SVR), is proposed. The main idea is that the PSO algorithm is used to select an approximation of the three parameters of the SVR model. The proposed method is tested using a dataset of converter input and output power. The most important features about the proposed algorithm are 1) low cost, easy to implement in current industrial system, 2) no additional hardware is required, and 3) no complicated algorithm is required. A comparison is made between the proposed method and the SVR method. The experimental setup has been developed and analyzed to show the effectiveness of the proposed method.

## 2. Electrolytic Capacitor's Degradation

The elements of ESR have characteristics that change in value depending on temperature and frequency. As the temperature increases, the polarization of the dielectric forming the electrolyte increases, and the mobility of the electrons increases, reducing the ESR. In addition, because the electrolyte expands thermally and the cross-sectional area increases, the equivalent series resistance decreases, and the capacitance increases. As the frequency increases, the loss due to the alignment of the polarized dipole increases, so the ESR decreases, and the capacitance tends to decrease slightly as the dielectric constant of the capacitor decreases [19,20]. Therefore, when using the ESR and capacitance value for the electrolytic capacitor diagnosis, it is necessary to consider the temperature and frequency characteristics to make accurate state diagnosis.

When the capacitor is charged/discharged, due to the charge/discharge current flowing, a loss due to the equivalent series resistance occurs and the temperature rises. When the temperature of the capacitor rises, the volume of the electrolyte increases. When the temperature rises above the rated value, the electrolyte leaks to the outside of the capacitor. When the temperature is room temperature, the amount of the electrolytic solution is reduced, and the cross-sectional area is reduced. The reduction of the electrolyte reduces the effective cross-sectional area, the equivalent series resistance increases, and the capacitance decreases.

As the ESR increases, the ripple voltage of the capacitor increases, which affects the control of the load and increases the power loss, thereby accelerating the temperature rise. When the capacitor is repeatedly charged/discharged, the electrolytic solution decreases due to this process, the capacitor deteriorates, and the capacitance decreases. Therefore, it is possible to judge the state of the capacitor by measuring the change of the capacitor ESR value. According to IEC 60384-4 [21], a general-grade electrolytic capacitor with a rated voltage of 160 V or more is judged to be faulty when the capacitance is reduced by more than 15% and the ESR is increased by more than three times [22].

The simplest and most inexpensive method of electrolytic capacitors studied to date is to determine the degree of deterioration from the magnitude of the ripple voltage across the capacitor. When the capacitor degrades, the ESR increases, and voltage ripple increases from the charge/discharge current. The degree of degradation of the capacitor can be determined from the amount of increase of the ripple [8,13]. However, since the influence of the load is included in addition to the deterioration information of the capacitor in the change of the ripple voltage, it is difficult to know exactly how much the capacitor is deteriorated.

### 3. PSO-SVR for Capacitor Condition Monitoring

This study aims to monitor the converter DC-link capacitor condition in back-to-back converters without removing the capacitor, using extra hardware, or injecting low-frequency voltage component to the DC-link voltage.

The basic idea of the proposed PSO-SVR method is to find a function or model that defines the relation between given DC-link voltage, grid voltages, grid currents, load voltages, load currents, and its corresponding capacitance is determined using a set of training data as shown in Figure 3. Through the offline training, a particular model is obtained to relate these inputs and output. The collected training sets are measured at different load posers, which are 1, 1.5, 2, 2.5, and 3 kW. This function is then used online to predict the output for the given inputs that are not included in the training set. Then, the capacitance can be calculated online with the input of the capacitor power. The modified PSO-SVR is used to estimate the capacitance, and since using SVR alone severely depends on the tuning of its parameters, the PSO algorithm is used, which enables a fast online-based approach with high parameter estimation accuracy. The training data are collected when the capacitance changed between 1950 [ $\mu\text{F}$ ] to 3950 [ $\mu\text{F}$ ].

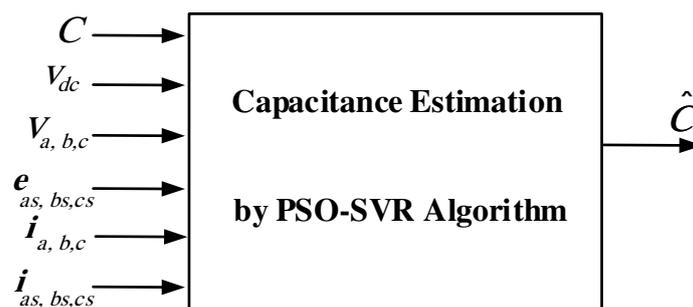


Figure 3. The structure of DC-link capacitor monitoring.

The SVR is an algorithm that estimates a function that predicts unknown mapping between a system's  $l$ -dimensional inputs and real output using a series of training data. When the map is accurately constructed, the relation between the inputs and output is then used to predict the target output. The regression algorithm approximates the unknown desired function in the following form [23,24]:

$$f(x) = (w \cdot \Phi(x)) + b \quad (1)$$

where  $w$  represents the weight vector and  $b$  is the bias. These values are extracted from the available training data to determine the target function. The first step is to obtain both  $w$  and  $b$  to minimize the

real data differences outside the insensitive region, which are determined by slack variables  $\xi$  and  $\xi^*$ . To minimize the empirical risk, the slack variables are applied as [25]

$$R_{reg}(f) = \frac{1}{2} \|w\|^2 + \sum_{i=1}^n \Gamma(f(x_i) - y_i) \tag{2}$$

subject to

$$\begin{aligned} y_i - w \cdot \Phi(x_i) - b &\leq \varepsilon + \xi_i^* \\ w \cdot \Phi(x_i) + b - y_i &\leq \varepsilon + \xi_i \\ i = 1, 2, \dots, n, \xi_i, \xi_i^* &\geq 0 \end{aligned} \tag{3}$$

where  $\Gamma(\cdot)$  is a cost function,  $\gamma$  is a constant determining the trade-off between minimizing training errors and minimizing the model complexity term  $\|w\|^2$ , and  $\varepsilon$  is the allowable error. SVR reduces the error to zero if the penalty factor  $\gamma$  is very high. This option leads to a complex model. However, a low  $\gamma$  leads to high estimation errors.

The regression problem in (3) is then solved using dual multipliers [26]:

$$f(x) = \sum_{i=1}^n (\alpha_i - \alpha_i^*) \times K(x_i, x) + b \tag{4}$$

where  $\alpha_i$  and  $\alpha_i^*$  are Lagrange multipliers. Furthermore,  $K(x_i, x_j) = \Phi(x_i)^T \Phi(x_j)$  is the kernel function that enables a dot product in high-dimensional feature space using low-dimensional space data input without calculating the function. The radial base function that is used in the framework is expressed as [27,28]

$$K(x_i, x) = \exp\left\{-\frac{|x_i - x|^2}{\sigma^2}\right\} \tag{5}$$

where  $\sigma$  is the RBF width.

The PSO method is a population-based search technique that starts with a population of random solutions [29]. Every solution within the swarm is called a particle [30]. The swarm is arbitrarily initialized and updated in each reiteration  $t$  to fully adapt the fitness function. The  $i^{th}$   $d$ -dimensional particle is characterized by its location vector  $P_i^t = P_{i1}^t, P_{i2}^t, \dots, P_{id}^t$  and its speed vector  $v_i^t = v_{i1}^t, v_{i2}^t, \dots, v_{id}^t$ . Each particle knows its best personal location  $P_{i,Best}^t$  and the entire population's best global solution  $G_{Best}^t$ . The position of the population at iteration  $t$  is  $P^t = P_1^t, P_2^t, \dots, P_N^t$ , where  $N$  is the population size. Each particle updates its location according to (6) and (7):

$$v_i^{t+1} = w^t v_i^t + c_1 r_1 (p_{i,Best}^t - p_i^t) + c_2 r_2 (p_{i,Best}^t - p_i^t) \tag{6}$$

$$p_i^{t+1} = p_i^t + v_i^{t+1}, i = 1, 2, 3, \dots, N \tag{7}$$

where  $c_1$  (cognition factor) and  $c_2$  (social learning factor) are constants,  $r_1$  and  $r_2$  are random values in the range  $[0, 1]$ , and  $w^t$  is the speed weight at iteration  $t$ . The constriction coefficients presented in [30] is used to set  $c_1$  and  $c_2$ :

$$x = \frac{2}{\varphi - 1 + \sqrt{\varphi^2 - 4\varphi}} \tag{8}$$

$$c_1 = x\varphi_1, c_2 = x\varphi_2 \tag{9}$$

where  $\varphi = \varphi_1 + \varphi_2 > 4$ . In this paper,  $\varphi_1 = \varphi_2 = 2.05$ ; thus,  $c_1 = c_2 = 1.4962$ . Location and speed boundaries are adjusted such that if a particle tries to pass the allowable boundaries, a limiting procedure brings it back within the permissible limit. If the best personal solution  $P_{i,Best}^t$  has a higher fitness than the current best global solution  $G_{Best}^{t-1}$ , then  $G_{Best}^t$  is equal to  $P_{i,Best}^t$ . The end points are

determined when the best global solution results in a permissible fitness or when a predetermined number of iterations is achieved.

The PSO is mainly used to tune and optimize the SVR parameters. A detailed description divided into steps is as follows:

Step 1: Initialize. The initial value of the parameter is established. The particles are set in the characteristics space. Each particle  $i$  is represented by  $x_i = \{\gamma, \sigma, \varepsilon\}$ .

Step 2: Conformity assessment. Once the coding procedure is completed, three values of the  $\gamma$ ,  $\sigma$ , and  $\varepsilon$  parameters are inserted in the SVR model to predict the problem. To avoid overuse, a cross-validation of K-fold (CV) is used in the training phase and a validity error is calculated. The training data points are selected as input variables and the current data are selected as output variables. First, the training datasets are entered into the proposed model.

Step 3: Update  $pbest$ . In the current iteration, if the physical condition value of particle  $i$  is greater than the physical condition value of  $pbest$ ,  $pbest$  is replaced by  $x_i$ .

Step 4: Update  $gbest$ . If the value of adjustment of  $pbest$  exceeds the value of adjustment of  $gbest$  in the current iteration,  $gbest$  is replaced by  $pbest$ .

Step 5: Update the speed. The velocity of each particle is calculated according to (6).

Step 6: Update your location. The position of each particle is calculated according to (7).

Step 7: Stop the criteria. The process is repeated in the order described above until the maximum iteration is reached. The framework of a PSO-SVR method is depicted in Figure 4 [31].

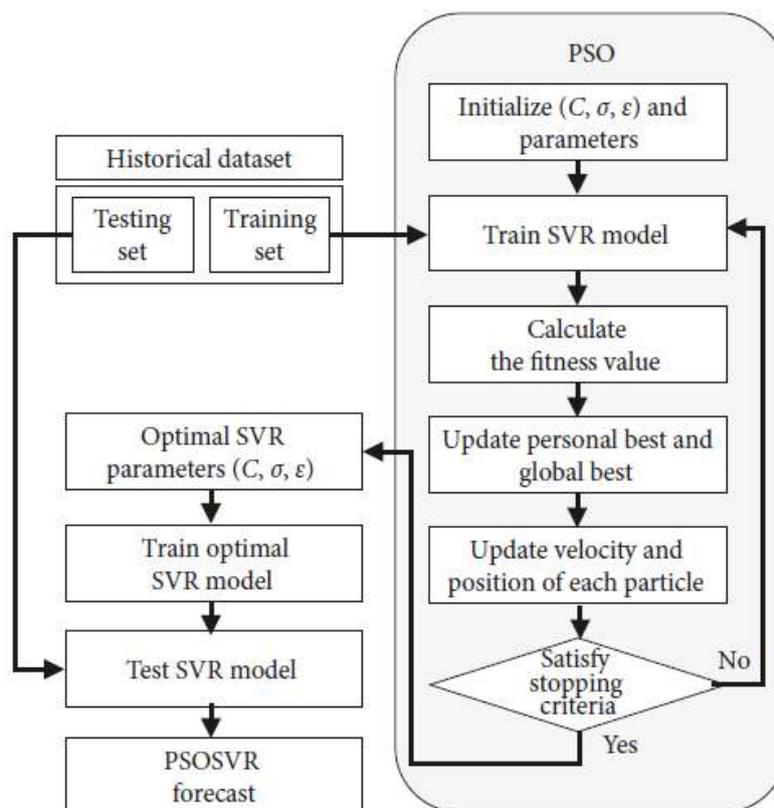


Figure 4. Process implementation diagram.

In order to find the coefficients in (10), parameters  $\sigma$ ,  $\gamma$ , and  $\varepsilon$  are obtained from the PSO algorithm. With such different parameter types, a separate equation for each parameter is used to update a different inertial weight. The PSO-SVR method updates the inertial weight as follows:

$$w_i^{t+1} = 0.95e^{-|P_{i,Best}^t - G_{Best}^t|^2}. \quad (10)$$

The summary of implementation process is shown in Figure 5. The main steps are as follows:

- (1) Generate offline data, which are the input and output information.
- (2) Initialize PSO. The  $P_0 = \{P_{01}, P_{02}, \dots, P_{0N}\}$  are randomly set, where  $N$  is the swarm population.
- (3) Train SVR using each particle as SVR parameters.
- (4) Calculate fitness, which is the root mean square error (RMSE) between the SVR prediction and output of test data.
- (5) Find personal and global best positions, and update weight.
- (6) Repeat steps until  $t$  reaches a set maximum number.
- (7) SVR prediction with optimal parameters obtained by PSO.

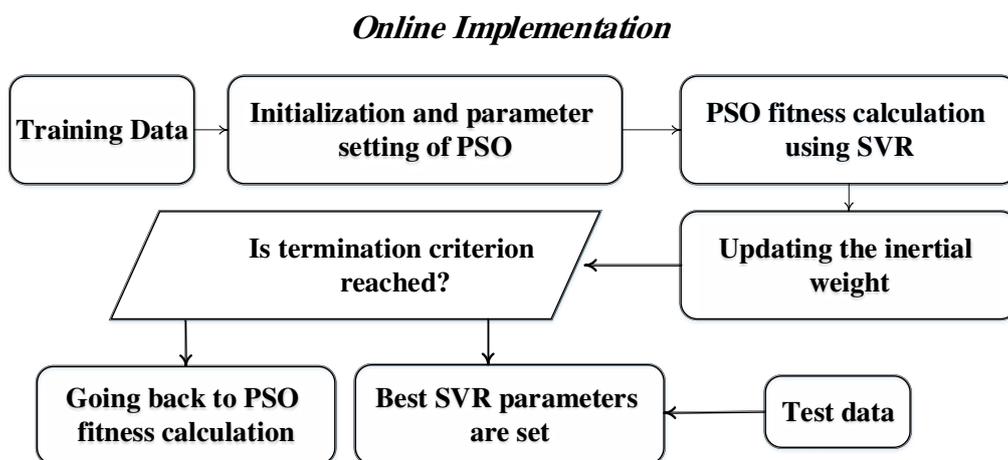


Figure 5. Implementation process diagram.

In the lab experiment, a capacitor bank consisting of five branches, that is, four parallel-connected capacitors of 500  $\mu\text{F}$  and two series connection in one branch of 3900  $\mu\text{F}$ , corresponding to the case A in Table 1, the initial capacitance is 3950  $\mu\text{F}$ . It is assumed that the lifetime of this capacitor bank ends when its capacitance is reduced to 1950  $\mu\text{F}$ . The training data are listed in Table 1, where  $C_1$  and  $C_2$  represent 1950  $\mu\text{F}$  and 500  $\mu\text{F}$  capacitors, respectively.

Table 1. Training data and estimation accuracy.

	Measured Value $\mu\text{F}$	SVR Estimated Value $\mu\text{F}$	PSOSVR Estimated Value $\mu\text{F}$
A	3789	3789	3789
B	3323	3323	3323
C	2857	2857	2857
D	2394	2394	2394
E	1928	1928	1928

Each of these five samples corresponds to the RMS input voltage, current, and DC-link, respectively. Due to the different loading conditions of the AC/DC/AC converter, five sets of 5k samples are used under the load levels of 1 kW, 1.5 kW, 2 kW, 2.5 kW, and 3 kW. The capacitor is estimated periodically by monitoring the abovementioned variables.

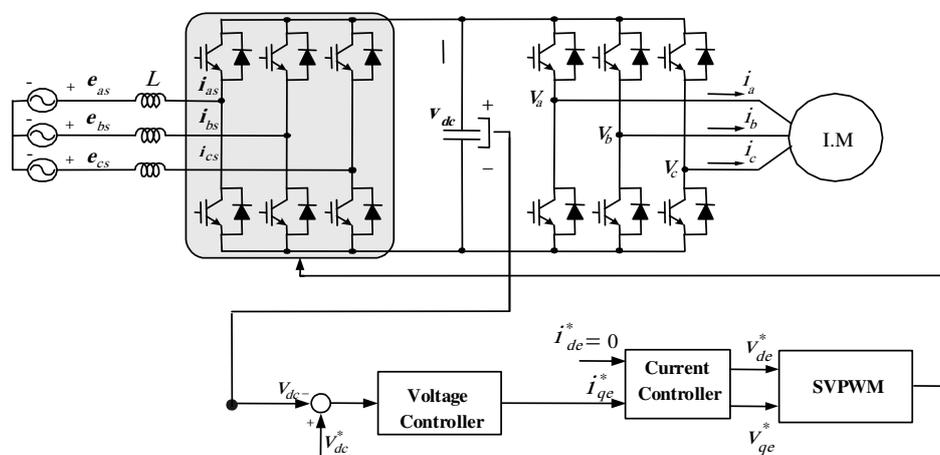
Table 2 shows a list of the predicted and measured data for the tested capacitor set. The proposed method shows a superior performance with estimation error less than 0.15% in different conditions.

**Table 2.** Capacitance estimation error.

Measured Value [ $\mu\text{F}$ ]	Estimated Value $\mu\text{F}$		Estimation Error [%]
	by SVR	by PSOSVR	
1928	1925	1927	0.05
2394	2397	2391	0.125

#### 4. Experimental Results

The experimental setup was constructed on a reduced scale in the laboratory and its configuration is shown in Figure 6. The converter parameters are listed in Table 3. A high-performance DSP chip TMS320C33 was used as a main controller, which operates at a 33.3 MHz clock and is capable of 32-bit floating-point operation. The sampling rate of the current control loop is double the PWM frequency; that is, the sampling period is 100  $\mu\text{s}$ . The space-vector modulation with symmetrical switching patterns was employed as a PWM strategy.

**Figure 6.** System hardware configuration.**Table 3.** System parameters.

Parameters	Value
Converter power rating	3 kVA
Input AC voltage	220 V
Input boost inductance	3.5 mH
Line resistance	0.5 $\Omega$
Switching frequency	5 kHz
Injected voltage	10 V peak, 30 Hz
DC-link voltage	340 V

Figure 7 shows the expected capacitance for transition from Case D to E in Table 1. For Case D, the expected capacitance is 2.391. This capacitance only contains an error of +0.125%. Table 1 shows the estimated capacitance with different methods. The errors of measurements in Figure 7 are listed in Table 2. For further investigation of PSO-SVR accuracy, a set of random values of capacitance estimation are implemented under five different power levels, and their actual and estimated values are shown in Table 4. In all cases, the estimation error is less than 0.2%. The proposed method is compared with the SVR method as shown in Figure 8. The percentage of errors for different power levels is shown in Figure 9. Table 5 shows the calculation of mean, maximum, minimum, standard deviation, and estimation errors in different loads.

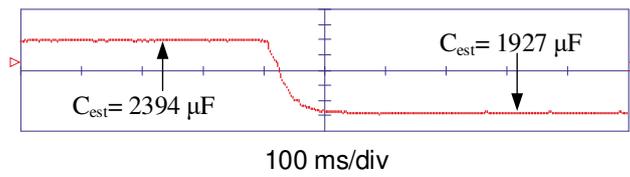


Figure 7. Capacitance estimation at abrupt variation of C using particle swarm optimization-based support vector regression (PSO-SVR).

Table 4. Real-time monitoring of DC-link capacitor for different input power.

Measured Capacitance [μF]	Power = 1 kW		Power = 1.5 kW		Power = 2 kW		Power = 2.5 kW		Power = 3 kW	
	Estimated Capacitance μF	Error	Estimated Capacitance μF	Error	Estimated Capacitance μF	Error	Estimated Capacitance μF	Error	Estimated Capacitance μF	Error
3789	3786	0.079%	3786	0.079%	3784	0.13%	3783	0.16%	3787	0.05%
3323	3319	0.06%	3322	0.11%	3319	0.25%	3324	0.03%	3321	0.06%
2857	2853	0.14%	2859	0.07%	2852	0.17%	2855	0.07%	2855	0.07%
2394	2391	0.125%	2396	0.08%	2392	0.13%	2391	0.125%	2392	0.08%
1928	1926	0.1%	1924	0.2%	1925	0.16%	1926	0.1%	1927	0.05%

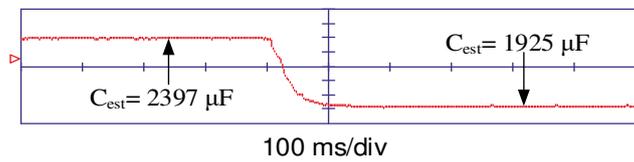


Figure 8. Capacitance estimation at abrupt variation of C using SVR.

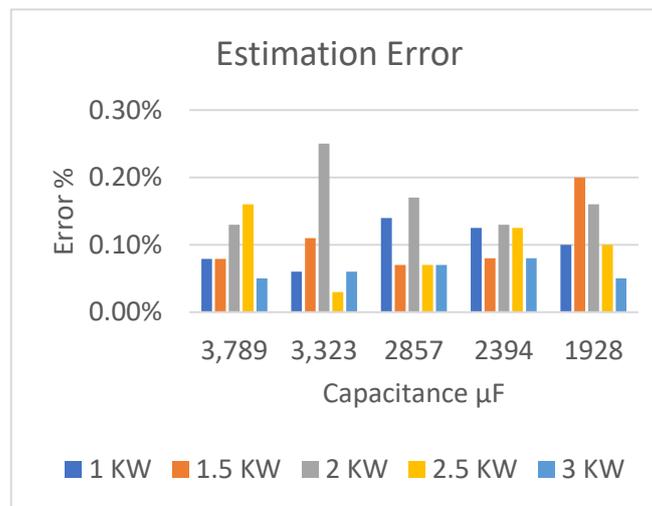


Figure 9. Capacitance estimation errors in different power.

Table 5. Capacitance monitoring error.

Measured Capacitance μF	Power = 1 kW	Power = 1.5 kW	Power = 2 kW	Power = 2.5 kW	Power = 3 kW	Mean	Max	Min	STD
	Error	Error	Error	Error	Error				
3789	0.079%	0.079%	0.13%	0.16%	0.05%	0.000917186	0.16%	0.05%	0.000397
3323	0.06%	0.11%	0.25%	0.03%	0.06%	0.001609477	1.00%	0.03%	0.004655
2857	0.14%	0.07%	0.17%	0.07%	0.07%	0.000960228	0.17%	0.07%	0.000427
2394	0.125%	0.08%	0.13%	0.125%	0.08%	0.001053874	0.13%	0.08%	0.000229
1928	0.1%	0.2%	0.16%	0.1%	0.05%	0.002759459	5.00%	0.10%	0.019444

### 5. Conclusions

A novel method for capacitance estimation of the electrolytic capacitor has been proposed for the three-phase back-to-back converters. The proposed scheme has been applied by software only, without

a need for removing the capacitor, sensing the capacitor current, or injecting a low-frequency component in the DC-link voltage reference. A particle swarm optimization-based support vector regression technique is used to estimate the capacitance value based on the input and output information such as voltage, current, power, as well as the DC-link voltage. An offline process for different capacitance values and several power levels is implemented to extract the relation between these variables and the DC-link capacitor. SVR then calculates the value of C online when using an input other than the trained sample. The error analysis can be tolerated if the maximum estimation error is less than 0.2%, which could be acceptable in many practical applications. However, in some cases, the estimation error exceeds the 0.2%. In this case, the online estimation process should be repeated and if the error still more than 0.2% and the offline process should be done again to modify the SVR parameters to reduce the estimation error. Such errors occasionally occur when the capacitor temperature is out of the tested and training range. The proposed method does not cover all the operating temperatures.

**Author Contributions:** For research articles with several authors, a short paragraph specifying their individual contributions must be provided. The following statements should be used “Conceptualization, A.G.A.-K.; Methodology, A.G.A.-K. and S.A.; Software, A.B.A.; Validation, A.G.A.-K., S.A. and A.A.; Formal Analysis, A.G.A.-K.; Investigation, A.G.A.-K.; Resources, A.G.A.-K.; Data Curation, A.A.; Writing-Original Draft Preparation, A.G.A.-K.; Writing-Review & Editing, S.A.; Visualization, S.A.; Supervision, Project Administration, A.G.A.-K.; Funding Acquisition, A.G.A.-K.”, please turn to the CRediT taxonomy for the term explanation. Authorship must be limited to those who have contributed substantially to the work reported.

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